



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al.

Art Unit : 2813

Serial No. : 09/898,986

Examiner : Laura Schillinger

Filed : July 3, 2001

Title : SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING THE
SAME**MAIL STOP AF**

Commissioner for Patents

P.O. Box 1450

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REPLY TO ACTION OF MARCH 13, 2003

In reply to the Final Office Action of March 13, 2003, and to the Interview Summary Form of January 27, 2004, Applicant submits the following remarks.

Claims 1-68 are currently pending, with claims 3-5, 8-11, 17-37, 39-45, and 47-63 being withdrawn and claims 1, 2 and 64 being independent. Applicant respectfully requests clarification of status of claims 55-63, which, as pointed out in Applicant's response of July 11, 2003, appear to have been incorrectly or inadvertently included in the list of withdrawn claims, as discussed in more detail in that response.

Claims 1, 2, 6, 7, 12-16, 38, and 46 are rejected under 35 U.S.C. 112, first paragraph, as containing subject matter which was not described in the specification in such a way as to enable one skilled in the art to make and/or use the invention. Specifically, the Office Action points to the claim language (emphasis in original): "introducing ion of a p-type impurity into at least a portion of only the first semiconductor island **without mass separation** wherein the portion is to become a channel region of a thin film transistor." Similarly, the Office Action states that "... Applicant lacks sufficient enablement for his amended clause," with specific reference to the emphasized clause, "without mass separation."

The Office Action then goes on to state that "...there is no disclosure within the specification of how Applicant's method prevents mass separation. In fact, the terms mass separation are not located within the specification and therefore Applicant's amended claim language is not properly enabled by the specification" (see Office Action, paragraphs bridging pages 2 and 3 and first full paragraph of page 3).